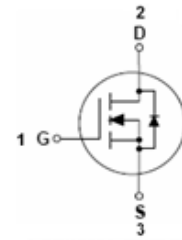


### Features

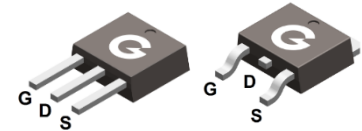
- Low power loss by high speed switching and low on-resistance
- Excellent thermal behavior
- HBM: JESD22-A114-B: 1B
- Product validation acc. JEDEC Standard
- RoHS compliant with Halogen-free

HF



### Applications

- PFC power supply stages
- Lighting applications
- Telecom
- Server
- UPS



SJM65R600I SJM65R600D

TO-251 TO-252

### Mechanical Data

- Case: TO-251, TO-252
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

## Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
SJM65R600I	TO-251	80 pcs / Tube	SJM65R600I
SJM65R600D	TO-252	80 pcs / Tube & 2500 pcs / Tape & Reel	SJM65R600D

## Maximum Ratings (@ T<sub>C</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>	650	V
Gate-to-Source Voltage	V <sub>GSS</sub>	±30	V
Continuous Drain Current (T <sub>C</sub> = 25°C)	I <sub>D</sub>	8	A
Continuous Drain Current (T <sub>C</sub> = 100°C)		5	A
Pulsed Drain Current (t <sub>p</sub> = 10μs, T <sub>C</sub> = 25°C)	I <sub>DM</sub>	32	A
Single Pulse Avalanche Energy <sup>*3</sup>	E <sub>AS</sub>	130	mJ
Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>D</sub>	63	W
Operating Junction Temperature Range	T <sub>J</sub>	-55 ~ +150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 ~ +150	°C

## Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction-to-Case	R <sub>θJC</sub>	-	1.3	2	°C/W
Thermal Resistance Junction-to-Air <sup>*1</sup>	R <sub>θJA</sub>	-	50	62	°C/W

### Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
$V_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	650	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 650V, V_{GS} = 0V$	-	-	1	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$R_{DS(ON)}$	Drain-Source On-resistance <sup>*2</sup>	$V_{GS} = 10V, I_D = 4A$	-	0.52	0.6	$\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.5	3.7	4.5	V
$R_G$	Gate Resistance	$V_{GS} = 0V, f = 1MHz$	-	8.3	-	$\Omega$
<b>Dynamic Characteristics</b>						
$C_{ISS}$	Input Capacitance	$V_{GS} = 0V$	-	415	-	pF
$C_{OSS}$	Output Capacitance	$V_{DS} = 40V$	-	46	-	
$C_{RSS}$	Reverse Transfer Capacitance	$f = 250KHz$	-	0.8	-	
<b>Switching Characteristics</b>						
$t_{d(ON)}$	Turn-on Delay Time <sup>*4</sup>	$V_{DD} = 400V$	-	6	-	ns
$t_r$	Turn-on Rise Time <sup>*4</sup>	$V_{GS} = 10V$	-	7	-	
$t_{d(OFF)}$	Turn-Off Delay Time <sup>*4</sup>	$I_D = 2.5A$	-	26	-	
$t_f$	Turn-Off Fall Time <sup>*4</sup>	$R_G = 10\Omega$	-	13	-	
$Q_G$	Total Gate-Charge	$V_{DD} = 480V$	-	13	-	nC
$Q_{GS}$	Gate to Source Charge	$V_{GS} = 10V$	-	2.4	-	
$Q_{GD}$	Gate to Drain (Miller) Charge	$I_D = 4A$	-	7.9	-	
<b>Source-Drain Diode Characteristics</b>						
$V_{SD}$	Diode Forward Voltage <sup>*2</sup>	$I_{SD} = 4A, V_{GS} = 0V$	-	0.85	1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F = 4A, V_{GS} = 0V$	-	220	-	ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt = 100A/\mu s$	-	1.8	-	$\mu C$

Notes:

1. The data tested by surface mounted on a minimum recommended FR-4 board
2. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
3. The  $E_{AS}$  data shows Max. rating. The test condition is  $V_{DD} = 100V, V_{GS} = 15V, L = 50mH$
4. Guaranteed by design, not subject to production

### Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

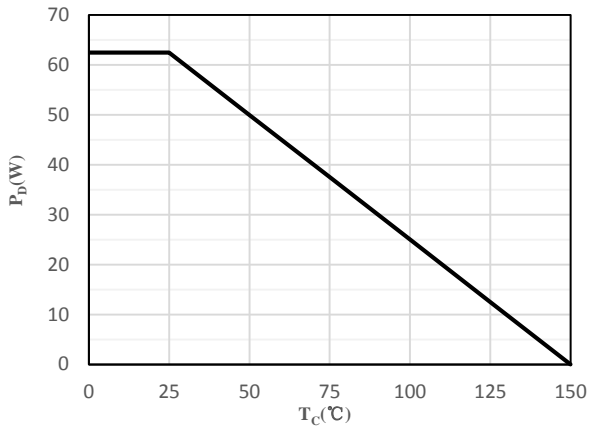


Fig 1 Power Dissipation

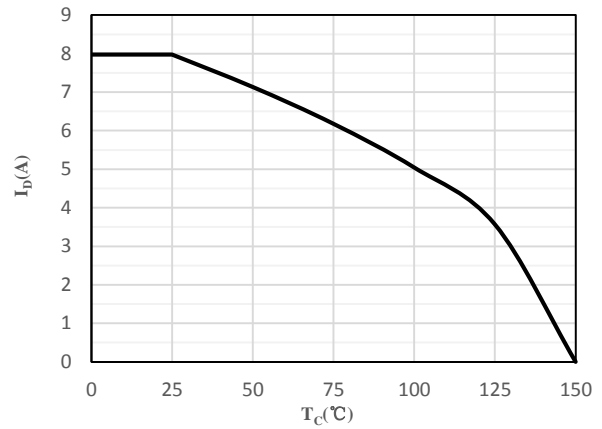


Fig 2 Drain Current

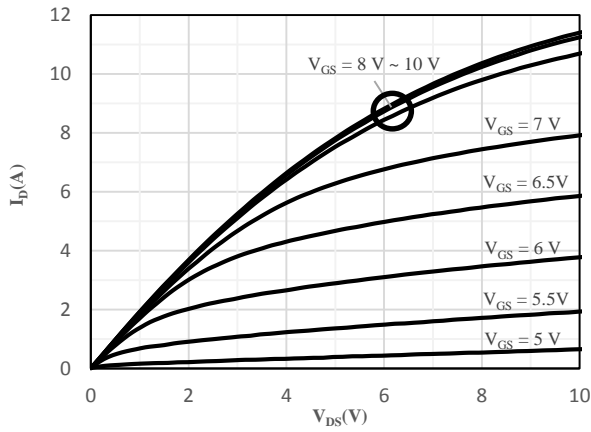


Fig 3 Typical Output Characteristics

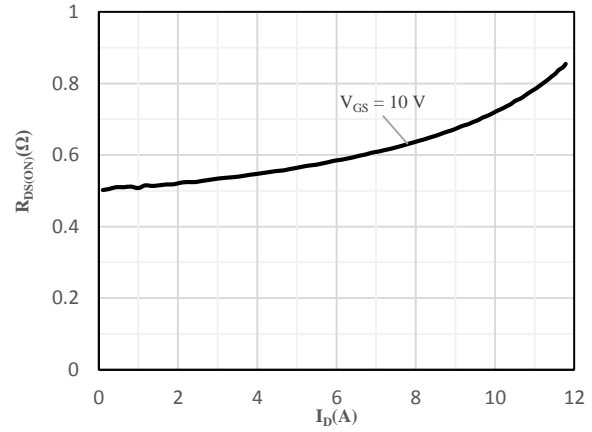


Fig 4 On-Resistance vs. Drain Current and Gate Voltage

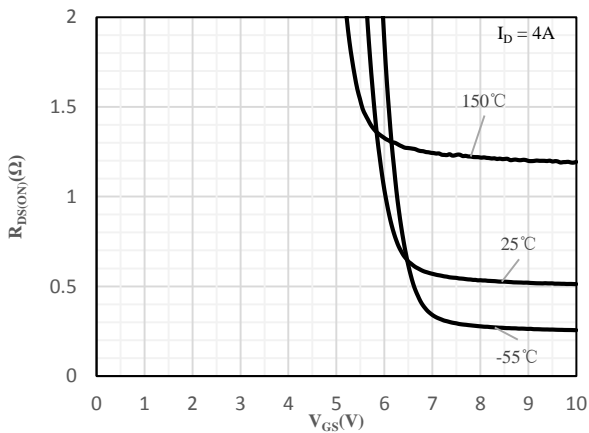


Fig 5 On-Resistance vs. Gate-Source Voltage

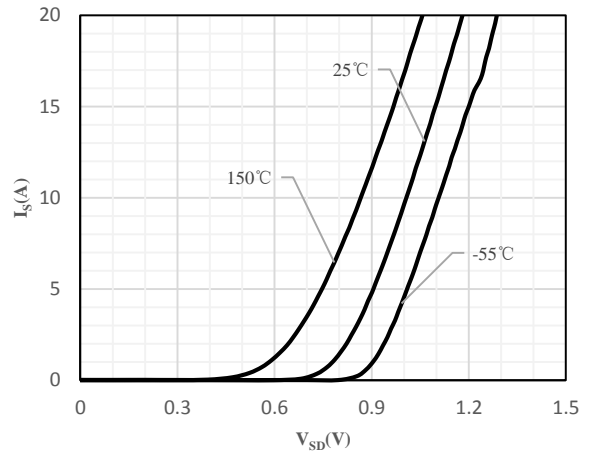
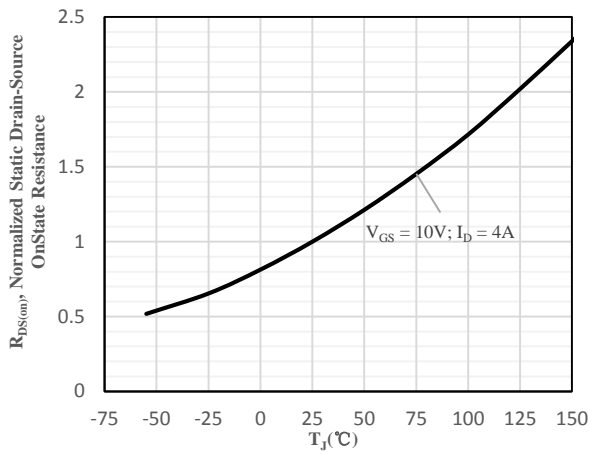
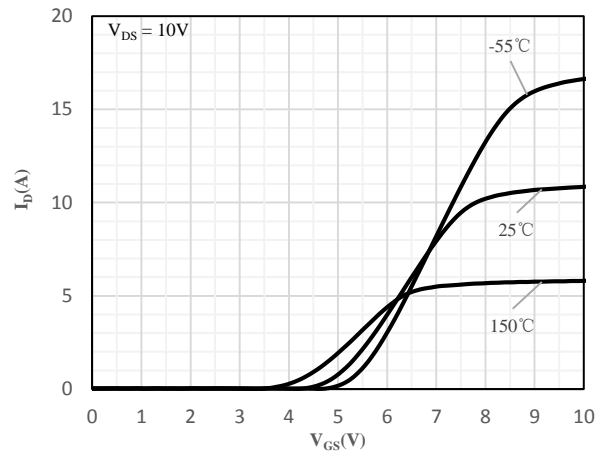


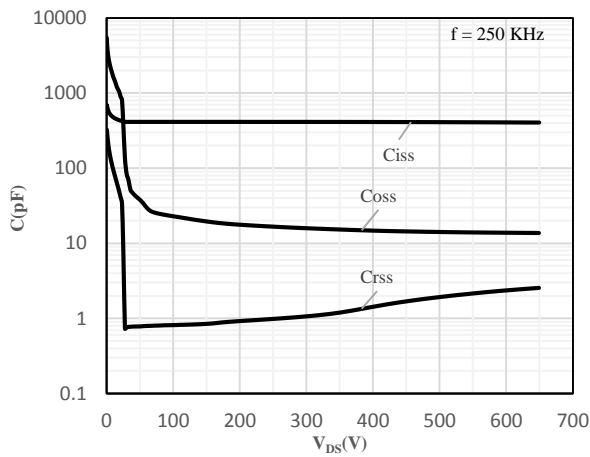
Fig 6 Body-Diode Characteristics



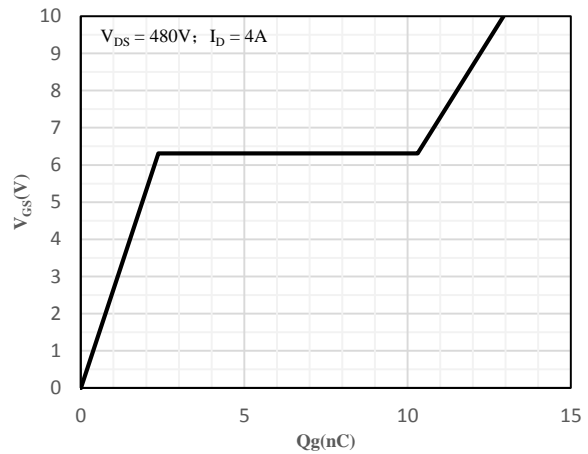
**Fig 7 Normalized On-Resistance vs. Junction Temperature**



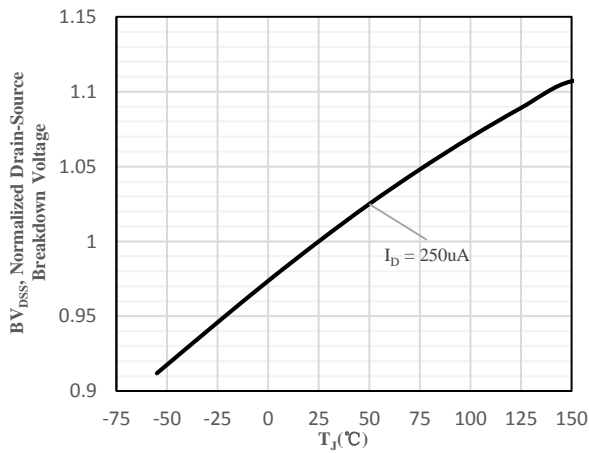
**Fig 8 Transfer Characteristics**



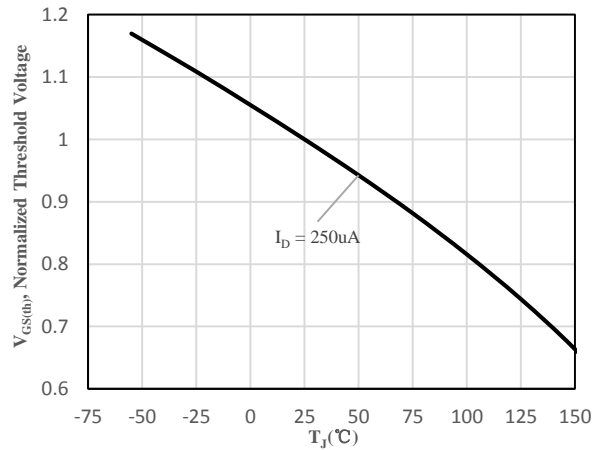
**Fig 9 Capacitance Characteristics**



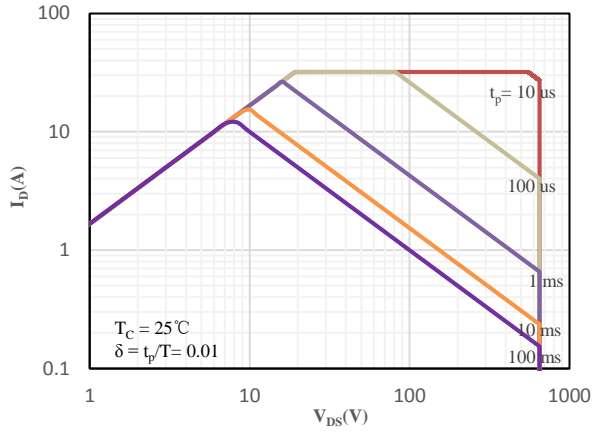
**Fig 10 Gate-Charge Characteristics**



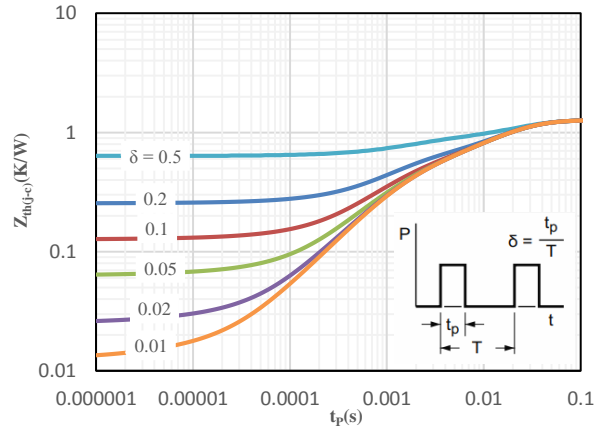
**Fig 11 Normalized Breakdown Voltage vs. Junction Temperature**



**Fig 12 Normalized  $V_{GS(th)}$  vs. Junction Temperature**

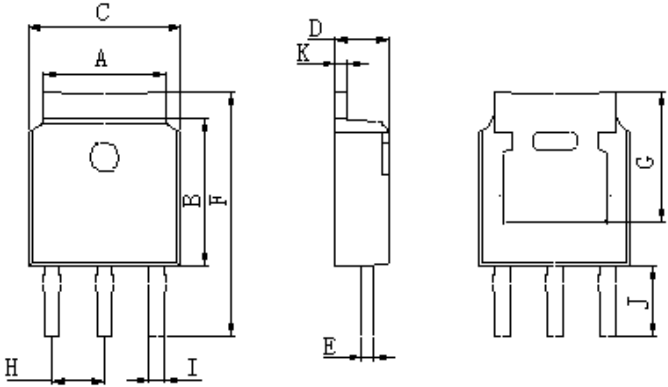


**Fig 13 Safe Operation Area**

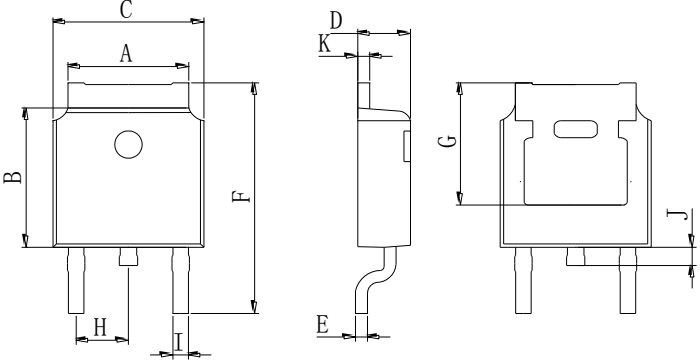


**Fig 14 Maximum transient thermal impedance**

Package Outline Dimensions (Unit: mm)

TO-251			
Dimension	Min.	Max.	
A	5.05	5.65	
B	5.80	6.40	
C	6.25	6.85	
D	2.20	2.40	
E	0.40	0.60	
F	11.00	11.60	
G	5.05	5.65	
H	2.10	2.50	
I	0.70	0.90	
J	4.00	4.40	
K	0.40	0.60	

TO-252			
Dimension	Min.	Max.	
A	5.05	5.65	
B	5.80	6.40	
C	6.25	6.85	
D	2.20	2.40	
E	0.40	0.60	
F	9.71	10.31	
G	5.05	5.65	
H	2.10	2.50	
I	0.70	0.90	
J	0.50	0.70	
K	0.40	0.60	

Mounting Pad Layout (Unit: mm)

TO-252
